



# Release Notes for Cisco D9854/D9854-I Advanced Program Receiver for Software Version 4.01

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## Introduction

This document describes the resolved caveats, open caveats, hash digest values, and upgrading the FPGA in software Version 4.01 for the Cisco D9854/D9854-I Advanced Program Receiver. This version supersedes Version 4.00, providing resolutions for several issues.

## New Features

There are no new features for the D9854/D9854-I in software Version 4.01.



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# Caveats

This section includes open and resolved caveats for this release. Links are provided to the Bug Toolkit where you can find details about each caveat.

This section includes the following topics:

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## Open Caveats - Version 4.01

The following is an open caveat in Version 4.01. The caveat ID links to the Cisco Bug Toolkit, where you can find details about the caveat.

- [CSCui91423](#)

When using Google Chrome Version 29.0 or higher, or Firefox Version 25.0 or higher, some functions of the web GUI are not supported properly.

## Resolved Caveats - Version 4.01

The following is a list of resolved caveats in Version 4.01. The caveat ID links to the Cisco Bug Toolkit, where you can find details about the caveat.

- [CSCui61278](#)

The units that decode the H.264 SD video and MPEG-1 Layer 2 audio are randomly rebooting.

- [CSCui77590](#)

The backup and restore fails when the MIB Object IDs do not match.

- [CSCuj95893](#)

There is a loss of audio on the AES output when the SBR is switched on or off.

- [CSCul03168](#)

The system time clock returns the wrong value, causing the video and audio to pause and skip every 5 to 10 minutes.

- [CSCul47517](#)

The unit may freeze when displaying the **About > Features** menu on the front panel. The right/left arrow buttons trigger a reboot.

- [CSCul53315](#)

The service replacement is not operational when tuned to a system with unknown descriptor in the stream.

- [CSCul63142](#)

The unit may discard all settings after an uncontrolled power cycle.

- [CSCul70100](#)

The audio driver stops attempting to correct the lip sync value after receiving a high offset limit value.

- [CSCuI90273](#)  
The main processor resets due to an I2C failure.
- [CSCuI94757](#)  
When a DPI event is received, it causes Cue Trigger 1 to momentarily close.
- [CSCuM10875](#) (D9854 only)  
The MOIP output does not work in Auto mode.
- [CSCuM16637](#)  
The Auto mode for MOIP output causes continuity count errors in the output stream.

## Upgrading the D9854 FPGA

We recommend that you upgrade the D9854 FPGA version to R00.00.05 after you upgrade the D9854 to software Version 4.01. This is for the D9854 receivers without IP input. For more information on upgrading the software version, see the *Cisco D9854/D9854-I Advanced Program Receiver Software Version 4.0 Installation and Configuration Guide*.



### Note

This upgrade can only be done through direct access to the unit.

- Step 1** From the user interface of the D9854, choose **System Settings > Versions**. The Versions page is displayed.

The screenshot displays the Cisco D9854 - Advanced Program Receiver web interface. The top navigation bar includes links for Summary, Input, Audio & Video, Transport Stream, System Settings, and Support. The left sidebar shows a menu with System, Alarms, Versions, Settings File, and Security/Accounts. The main content area is titled 'Versions' and contains several sections:

- Current Versions:** A table showing the current versions of the SW (4.01), FPGA (R00.00.03az), and HDR (4.01).
- Version Controls:** A section with dropdown menus for selecting the SW Version (currently 2.52) and FPGA version (currently R00.00.03az).
- Main Board - Firmware Versions:** A table listing various board components and their versions, including Boot5514 (B04.01.02DD), Boot7109 (B00.01.01DD), CPLD-5514 (E00.02.00), CPLD-7109 (E00.04.00), FP-PIC (1.04), FPGA Type (XC4VFX60), and FPGA ID (Wildcard FPGA ID).
- HW Board Versions:** A table showing hardware board details for the HDR board, including Rev (1), Option Bits, and Serial Nr (AAEJSHE).
- APP / FPGA Downloads:** A section with a 'Browse...' button and a 'Download' button. A note states: 'NOTE: Device will not reboot automatically following a FPGA download. Device will reboot automatically following an APP download.'
- Download Status:** A table with columns for Triggered, Received, Rejected, Total CDT, and State. A 'History' button is also present.

- Step 2** In the **APP/FPGA Downloads** area, click **Browse**.

- Step 3** Choose the FPGA R00.00.05 CDT file and click **Open**. The FPGA file must match the unit's SDI version, MPEGoIP version, or both. For more information, contact Cisco Services.
- Step 4** Click **Download** to download the upgrade file. The **State** field in the **Download Status** area displays the progress of the download. When the download is complete, a Successfully Completed message is displayed at the top of the Versions page.
- Step 5** From the **FPGA** drop-down list, choose the new FPGA firmware Version R00.00.05. The Select Version dialog box is displayed.
- Step 6** Click **Select & Reboot**. The D9854 receiver will reboot and use the new FPGA version.

## D9854/D9854-I Hash Digest Values

You can generate the hash digest to verify the integrity of the software and FPGA versions running on the D9854/D9854-I receiver. For more information on generating the hash digest values, see the *Cisco D9854/D9854-I Advanced Program Receiver Software Version 4.0 Installation and Configuration Guide*.

Table 1 lists the official D9854 v4.01 hash digest values.

**Table 1** *D9854 v4.01 Hash Digest Values*

Code/Processor	Hash Digest	Version
APP/5514	f63bab4f 63335657 c7159afa 998411fc fc831f6f	R04.01.00DD
APP/7109	98178c55 89f0c043 ffd988c0 b4ae064d 253868d4	R04.01.00DD
FPGA FX60/7109	41cd4328 6bc4cb16 cd1fdcd8 eaad78f6 65cfc05f	R00.00.03az (MPEG over IP)
FPGA FX60/7109	AE9D1429 8409E31F 90B6FCFD 77D9101F 4B4DE638	R00.00.05az (SDI)
FPGA FX60/7109	3bb3523e b4954d1b 382913ce c1fad497 ff5fc115	R00.00.02Vu (SFN)
FPGA FX100/7109	9842038e 20a58cf3 a41922ee 1770381b c6ab0d0e	R00.00.05az (MPEG and SDI)

Table 2 lists the official D9854-I v4.01 hash digest values.

**Table 2** *D9854-I v4.01 Hash Digest Values*

Code/Processor	Hash Digest	Version
APP/5514	f63bab4f 63335657 c7159afa 998411fc fc831f6f	R04.01.00DD
APP/7109	98178c55 89f0c043 ffd988c0 b4ae064d 253868d4	R04.01.00DD
FPGA LX155T/7109	2ffb9d0 095d8aca 36ff301e 39971d5a b2b5ee28	R00.00.01az
FEC FPGA/7109	582220BD C3CB4A25 3F610F63 3FCFB3DA F87801BB	R00.00.01vi

If these values do not match the generated digest values, contact Cisco Services.

## Related Documentation

*Cisco D9854/D9854-I Advanced Program Receiver Software Version 4.0 Installation and Configuration Guide*

*Open Source Used In Cisco D9854/D9854-I Advanced Program Receiver and Cisco D9824 Advanced Multi Decryption Receiver Software Version 4.0*

## Obtaining Documentation and Submitting a Service Request

For information on obtaining documentation, using the Cisco Bug Search Tool (BST), submitting a service request, and gathering additional information, see *What's New in Cisco Product Documentation* at: <http://www.cisco.com/en/US/docs/general/whatsnew/whatsnew.html>.

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