Cisco D9824 Advanced Multi Decryption Receiver Software Version 4.00 Release Note

Overview

This document describes the improvements, resolved issues, and known issues in software Version 4.00 for the Cisco D9824 Advanced Multi Decryption Receiver. This version supersedes Version 3.96, providing resolutions for several issues.

New Features

The following is a list of new features for the D9824 in software Version 4.00:

- MPEG Transport Stream over IP input (new D9824 hardware only)
- Single Program Transport Stream (SPTS) output capabilities on IP output
- Pro-MPEG Forum CoP#3/SMPTE ST 2022-1:2007 Forward Error Correction (FEC) support on IP input and IP output (new D9824 hardware only)
- Redundant MPEGoIP and Multiprotocol Encapsulation (MPE) outputs

Improvements and Maintenance

The following improvement is included in software Version 4.00:

- Updated web GUI

Resolved Issues

The following issues are resolved in Version 4.00:

ID	Description
CSCug85023	**No WSS data in Auto AFD mode when no AFD present**
Issue	When Auto AFD mode is selected and no AFD is present, the WSS data is not provided.
CSCuc29147	**SSH: Periodic Nmap port 22 probes causes a reset within an hour**
Issue	If the scripts that are being executed uses periodic nmap commands on port 22, the unit may reboot.
CSCui42734	**DPI messages are corrupted when DPI position in PMT changed**
Issue	If DPI PID is scrambled and its position is changed in PMT, the DPI messages on the output of the decoder are corrupted.
CSCug49196	**DPM: When ASI set to No Output, Inband Control drops MOIP PEs**
Issue	When the ASI Output Mode is set to No Output, and the unit is under Inband Control, the MPEG over IP output stops after a period of time or after you power cycle the unit.
CSCue88585	**Backup save fails when using Internet Explorer 8**
Issue	The backup Save function via Internet Explorer 8 does not work.
CSCzr07292	**AFD conversion needs to be synchronized with SMPTE-2016-1**
Issue	AFD conversion on the D9824, D9854/D9854-I, and the D9858 needs to be synchronized with the SMPTE-2016-1 standard.
CSCzr07391	**Backup fails with Windows 2008 FTP server (v7.5 FTP server)**
Issue	The FTP backup fails in the Windows 2008 R2 environment.

Known Issues

The following table describes a known issue with Version 4.00. The issue is provided with a description, how it is provoked, and resolved:

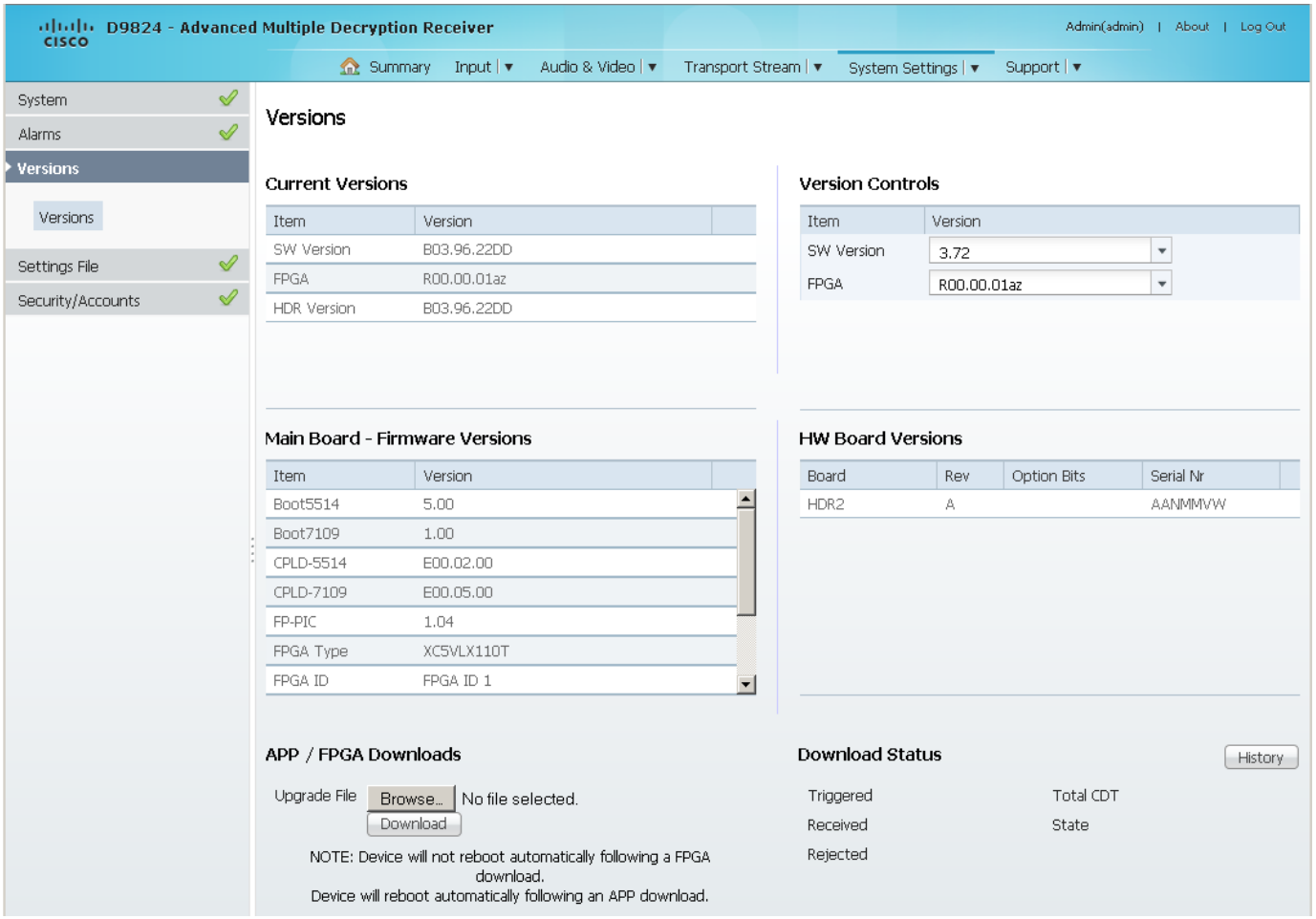
ID	Description
CSCui91423	**Web GUI: Google Chrome – Input Setup and Summary pages do not load**
Issue	When using Google Chrome, some functions of the web GUI are not supported properly.
How to provoke	Use Google Chrome Version 29 to access the web GUI and browse to the Input Setup and the Summary pages.
How to resolve	Use Mozilla Firefox or Internet Explorer to access the web GUI.

Upgrading the D9824 FPGA

We recommend that you upgrade the D9824 LX110T FPGA version to R00.00.03 after you upgrade the D9824 to software Version 4.00. This is for the older D9824 receivers without MPEGoIP input or FEC support. For more information on upgrading the software version, see the *Cisco D9824 Advanced Multi Decryption Receiver Software Version 4.0 Installation and Configuration Guide*.

Note: This upgrade can only be done through direct access to the unit.

1 From the user interface of the D9824, choose **System Settings > Versions**. The Versions page is displayed.



2 In the **APP/FPGA Downloads** area, click **Browse**.

3 Choose the FPGA R00.00.03 CDT file and click **Open**. For more information on the CDT file, contact Cisco customer support.

4 Click **Download** to download the upgrade file. The **State** field in the **Download Status** area displays the progress of the download. When the download is complete, a Successfully Completed message is displayed at the top of the Versions page.

5 From the **FPGA** drop-down list, choose the new FPGA firmware Version R00.00.03. The Select Version dialog box is displayed.

6 Click **Select & Reboot**. The D9824 receiver will reboot and use the new FPGA version.

D9824 Hash Digest Values

You can generate the hash digest to verify the integrity of the software and FPGA versions running on the D9824 receiver. For more information on generating the hash digest values, see the *Cisco D9824 Advanced Multi Decryption Receiver Software Version 4.0 Installation and Configuration Guide*.

The following is a list of the official D9824 v4.0 hash digest values:

Code/Processor	Hash Digest	Version
APP/5514	30cd140a 5ef0c001 b01c708b f360c321 90d291e8	R04.00.00DD
APP/7109	f1373e02 a2716c4b 5eb6a106 046c0626 63e9f41b	R04.00.00DD
FPGA LX110T/7109	41c88170 b0f4fb1b 329093ef 65bccf63 83d34470	R00.00.03az

The following is a list of the official D9824 v4.0, with FEC hash digest values:

Code/Processor	Hash Digest	Version
APP/5514	30cd140a 5ef0c001 b01c708b f360c321 90d291e8	R04.00.00DD
APP/7109	f1373e02 a2716c4b 5eb6a106 046c0626 63e9f41b	R04.00.00DD
FPGA LX155T/7109	2ffbf9d0 095d8aca 36ff301e 39971d5a b2b5ee28	R00.00.01az
FEC FPGA/7109	c9e3fd86 1b8f53e7 a2c67d09 3d0aed88 f9871985	R00.00.01vi

If these values do not match the generated digest values, contact Cisco customer support.

For Information

If You Have Questions

If you have technical questions, contact Cisco Services for assistance. Follow the menu options to speak with a service engineer.

Americas Headquarters
Cisco Systems, Inc.
170 West Tasman Drive
San Jose, CA 95134-1706
USA

http://www.cisco.com
Tel: 408 526-4000
800 553-6387
Fax: 408 527-0883

Cisco and the Cisco logo are trademarks or registered trademarks of Cisco and/or its affiliates in the U.S. and other countries. A listing of Cisco's trademarks can be found at **www.cisco.com/go/trademarks**.
Third party trademarks mentioned are the property of their respective owners.
The use of the word partner does not imply a partnership relationship between Cisco and any other company. (1110R)
Product and service availability are subject to change without notice.

© 2013 Cisco and/or its affiliates. All rights reserved.

October 2013 Part Number OL-29016-01