



Cisco D9854/D9854-I Advanced Program Receiver Software Version 4.00 Release Note

Overview

This document describes the improvements, resolved issues, and known issues in software Version 4.00 for the Cisco D9854/D9854-I Advanced Program Receiver. This version supersedes Version 3.96, providing resolutions for several issues.

New Features

The following is a list of new features for the D9854-I in software Version 4.00:

- MPEG Transport Stream over IP input
- Single Program Transport Stream (SPTS) output capabilities on IP output
- Pro-MPEG Forum CoP#3/SMPTE ST 2022-1:2007 Forward Error Correction (FEC) support on IP input and IP output
- Redundant MPEGoIP and Multiprotocol Encapsulation (MPE) outputs

Improvements and Maintenance

The following improvements and fixes are included in software Version 4.00:

- Additional Vertical Ancillary Data Space (VANC) support
- SDI output jitter improvements (D9854-I only)
- Higher throughput (180 Mbps) on new hardware (D9854-I only)
- Simplified DPM configuration with Automapping and Fixed Mapping options
- Relay control for SCTE-35
- Updated web GUI

Resolved Issues

The following issues are resolved in Version 4.00:

| ID | Description |
|-------------------|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| CSCug85023 | No WSS data in Auto AFD mode when no AFD present |
| Issue | When Auto AFD mode is selected and no AFD is present, the WSS data is not provided. |
| CSCuc29147 | SSH: Periodic Nmap port 22 probes causes a reset within an hour |
| Issue | If the scripts that are being executed uses periodic nmap commands on port 22, the unit may reboot. |
| CSCui42734 | DPI messages are corrupted when DPI position in PMT changed |
| Issue | If DPI PID is scrambled and its position is changed in PMT, the DPI messages on the output of the decoder are corrupted. |
| CSCug49196 | DPM: When ASI set to No Output, Inband Control drops MOIP PEs |
| Issue | When the ASI Output Mode is set to No Output, and the unit is under Inband Control, the MPEG over IP output stops after a period of time or after you power cycle the unit. |
| CSCug52348 | Combine multiple SCTE-35 messages into one SCTE-104 message |
| Issue | In the case where multiple SCTE-35 messages are being delivered to a decoder in rapid succession, the SCTE-35 messages should be packed as multiple loops within one SCTE-104 packet for insertion into the VANC. Currently the D9854 creates a unique SCTE-104 packet for every SCTE-35 packet. |
| CSCue88585 | Backup save fails when using Internet Explorer 8 |
| Issue | The backup Save function via Internet Explorer 8 does not work. |
| CSCZr07292 | AFD conversion needs to be synchronized with SMPTE-2016-1 |
| Issue | AFD conversion on the D9824, D9854/D9854-I, and the D9858 needs to be synchronized with the SMPTE-2016-1 standard. |
| CSCZr07391 | Backup fails with Windows 2008 FTP server (v7.5 FTP server) |
| Issue | The FTP backup fails in the Windows 2008 R2 environment. |

Known Issues

The following table describes a known issue with Version 4.00. The issue is provided with a description, how it is provoked, and resolved:

| ID | Description |
|----------------|---------------------------------------------------------------------------------------------------------|
| CSCui91423 | Web GUI: Google Chrome - Input Setup and Summary pages do not load |
| Issue | When using Google Chrome, some functions of the web GUI are not supported properly. |
| How to provoke | Use Google Chrome Version 29 to access the web GUI and browse to the Input Setup and the Summary pages. |
| How to resolve | Use Mozilla Firefox or Internet Explorer to access the web GUI. |

Upgrading the D9854 FPGA

We recommend that you upgrade the D9854 FPGA version to R00.00.05 after you upgrade the D9854 to software Version 4.00. This is for the D9854 receivers without IP input. For more information on upgrading the software version, see the *Cisco D9854/D9854-I Advanced Program Receiver Software Version 4.0 Installation and Configuration Guide*.

Note: This upgrade can only be done through direct access to the unit.

- 1 From the user interface of the D9854, choose **System Settings > Versions**. The Versions page is displayed.

The screenshot shows the 'Versions' page of the D9854 - Advanced Program Receiver. The page is divided into several sections:

- Current Versions:** A table showing the current versions of the SW, FPGA, and HDR.
- Version Controls:** A section with dropdown menus to select the version for the SW and FPGA.
- Main Board - Firmware Versions:** A table showing the firmware versions for various components like Boot, CPLD, FP-PIC, and FPGA Type.
- HW Board Versions:** A table showing the hardware board versions for the Board, Rev, Option Bits, and Serial Nr.
- APP / FPGA Downloads:** A section with a 'Browse...' button to select a file for download. It also includes a 'Download' button and a note about automatic rebooting.
- Download Status:** A section showing the status of the download, including 'Triggered', 'Received', and 'Rejected'.

- 2 In the **APP/FPGA Downloads** area, click **Browse**.
- 3 Choose the FPGA R00.00.05 CDT file and click **Open**. The FPGA file must match the unit's SDI version, MPEGoIP version, or both. For more information, contact Cisco customer support.
- 4 Click **Download** to download the upgrade file. The **State** field in the **Download Status** area displays the progress of the download. When the download is complete, a Successfully Completed message is displayed at the top of the Versions page.
- 5 From the **FPGA** drop-down list, choose the new FPGA firmware Version R00.00.05. The Select Version dialog box is displayed.
- 6 Click **Select & Reboot**. The D9854 receiver will reboot and use the new FPGA version.

D9854/D9854-I Hash Digest Values

You can generate the hash digest to verify the integrity of the software and FPGA versions running on the D9854/D9854-I receiver. For more information on generating the hash digest values, see the *Cisco D9854/D9854-I Advanced Program Receiver Software Version 4.0 Installation and Configuration Guide*.

The following is a list of the official D9854 v4.0 hash digest values:

| Code/Processor | Hash Digest | Version |
|-----------------|----------------------------------------------|----------------------------|
| APP/5514 | 30cd140a 5ef0c001 b01c708b f360c321 90d291e8 | R04.00.00DD |
| APP/7109 | f1373e02 a2716c4b 5eb6a106 046c0626 63e9f41b | R04.00.00DD |
| FPGA FX60/7109 | 41cd4328 6bc4cb16 cd1fdcd8 eaad78f6 65cfc05f | R00.00.03az (MPEG over IP) |
| FPGA FX60/7109 | ae9d1429 8409e31f 90b6fcfd 77d9101f 4b4de638 | R00.00.05az (SDI) |
| FPGA FX60/7109 | 3bb3523e b4954d1b 382913ce c1fad497 ff5fc115 | R00.00.02Vu (SFN) |
| FPGA FX100/7109 | 9842038e 20a58cf3 a41922ee 1770381b c6ab0d0e | R00.00.05az (MPEG and SDI) |

The following is a list of the official D9854-I v4.0 hash digest values:

| Code/Processor | Hash Digest | Version |
|------------------|----------------------------------------------|-------------|
| APP/5514 | 30cd140a 5ef0c001 b01c708b f360c321 90d291e8 | R04.00.00DD |
| APP/7109 | f1373e02 a2716c4b 5eb6a106 046c0626 63e9f41b | R04.00.00DD |
| FPGA LX155T/7109 | 2ffbf9d0 095d8aca 36ff301e 39971d5a b2b5ee28 | R00.00.01az |
| FEC FPGA/7109 | c9e3fd86 1b8f53e7 a2c67d09 3d0aed88 f9871985 | R00.00.01vi |

If these values do not match the generated digest values, contact Cisco customer support.

For Information

If You Have Questions

If you have technical questions, contact Cisco Services for assistance. Follow the menu options to speak with a service engineer.



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