

# снартег 12

# **SONET Topologies and Upgrades**



The terms "Unidirectional Path Switched Ring" and "UPSR" may appear in Cisco literature. These terms do not refer to using Cisco ONS 15xxx products in a unidirectional path switched ring configuration. Rather, these terms, as well as "Path Protected Mesh Network" and "PPMN," refer generally to Cisco's path protection feature, which may be used in any topological network configuration. Cisco does not recommend using its path protection feature in any particular topological network configuration.

This chapter explains Cisco ONS 15454 SONET topologies and upgrades. To provision topologies, refer to the *Cisco ONS 15454 Procedure Guide*.

Chapter topics include:

- 12.1 SONET Rings and TCC2/TCC2P Cards, page 12-1
- 12.2 Bidirectional Line Switched Rings, page 12-3
- 12.3 Path Protection, page 12-14
- 12.4 Dual-Ring Interconnect, page 12-19
- 12.5 Comparison of the Protection Schemes, page 12-28
- 12.6 Subtending Rings, page 12-29
- 12.7 Linear ADM Configurations, page 12-31
- 12.8 Path-Protected Mesh Networks, page 12-31
- 12.9 Four-Shelf Node Configurations, page 12-33
- 12.10 STS around the Ring, page 12-34
- 12.11 OC-N Speed Upgrades, page 12-35
- 12.12 In-Service Topology Upgrades, page 12-40
- 12.13 Overlay Ring Circuits, page 12-43

# 12.1 SONET Rings and TCC2/TCC2P Cards

Table 12-1 shows the SONET rings that can be created on each ONS 15454 node using redundant TCC2/TCC2P cards.

Ring Type	Maximum Rings per Node
BLSRs	5
2-Fiber BLSR	5
4-Fiber BLSR	1
Path protection with SDCC	34 <sup>1</sup> , <sup>2</sup>
Path protection with LDCC	14 <sup>3</sup> , <sup>4</sup>

#### Table 12-1ONS 15454 Rings with Redundant TCC2/TCC2P Cards

Ring Type	Maximum Rings per Node				
Path protection with LDCC and SDCC	26 <sup>5</sup>				
1. Total SDCC usage must be equal to or less th	an 68 SDCCs.				
2. See the "12.3 Path Protection" section on page	ge 12-14.				
3. Total LDCC usage must be equal to or less th	an 28 LDCCs.				
4. See the "12.3 Path Protection" section on page	ge 12-14.				
SDCC termination is allowed on the same port,	to or less than 84. When LDCC is provisioned, an but is not recommended. Using SDCC and LDCC on upgrade if the other end of the link does not support				

LDCC. You can provision SDCCs and LDCCs on different ports in the same node.

#### Table 12-1 ONS 15454 Rings with Redundant TCC2/TCC2P Cards (continued)

## **12.2 Bidirectional Line Switched Rings**

The ONS 15454 can support five concurrent bidirectional line switch rings (BLSRs) in one of the following configurations:

- Five two-fiber BLSRs
- Four two-fiber and one four-fiber BLSR

Each BLSR can have up to 32 ONS 15454s. Because the working and protect bandwidths must be equal, you can create only OC-12 (two-fiber only), OC-48, or OC-192 BLSRs.

Note

For best performance, BLSRs should have one LAN connection for every ten nodes in the BLSR.

#### 12.2.1 Two-Fiber BLSRs

In two-fiber BLSRs, each fiber is divided into working and protect bandwidths. For example, in an OC-48 BLSR (Figure 12-1), STSs 1 to 24 carry the working traffic, and STSs 25 to 48 are reserved for protection. Working traffic (STSs 1 to 24) travels in one direction on one fiber and in the opposite direction on the second fiber. The Cisco Transport Controller (CTC) circuit routing routines calculate the shortest path for circuits based on many factors, including user requirements, traffic patterns, and distance. For example, in Figure 12-1, circuits going from Node 0 to Node 1 typically travel on Fiber 1, unless that fiber is full, in which case circuits are routed on Fiber 2 through Node 3 and Node 2. Traffic from Node 0 to Node 2 (or Node 1 to Node 3) can be routed on either fiber, depending on circuit provisioning requirements and traffic loads.



The SONET K1, K2, and K3 bytes carry the information that governs BLSR protection switches. Each BLSR node monitors the K bytes to determine when to switch the SONET signal to an alternate physical path. The K bytes communicate failure conditions and actions taken between nodes in the ring.

If a break occurs on one fiber, working traffic targeted for a node beyond the break switches to the protect bandwidth on the second fiber. The traffic travels in a reverse direction on the protect bandwidth until it reaches its destination node. At that point, traffic is switched back to the working bandwidth.

Figure 12-2 shows a traffic pattern sample on a four-node, two-fiber BLSR.



Figure 12-2 Four-Node, Two-Fiber BLSR Traffic Pattern Sample

Figure 12-3 shows how traffic is rerouted following a line break between Node 0 and Node 3.

- All circuits originating on Node 0 that carried traffic to Node 2 on Fiber 2 are switched to the protect bandwidth of Fiber 1. For example, a circuit carrying traffic on STS-1 on Fiber 2 is switched to STS-25 on Fiber 1. A circuit carried on STS-2 on Fiber 2 is switched to STS-26 on Fiber 1. Fiber 1 carries the circuit to Node 3 (the original routing destination). Node 3 switches the circuit back to STS-1 on Fiber 2 where it is routed to Node 2 on STS-1.
- Circuits originating on Node 2 that normally carried traffic to Node 0 on Fiber 1 are switched to the protect bandwidth of Fiber 2 at Node 3. For example, a circuit carrying traffic on STS-2 on Fiber 1 is switched to STS-26 on Fiber 2. Fiber 2 carries the circuit to Node 0 where the circuit is switched back to STS-2 on Fiber 1 and then dropped to its destination.



Figure 12-3 Four-Node, Two-Fiber BLSR Traffic Pattern Following Line Break

#### 12.2.2 Four-Fiber BLSRs

Four-fiber BLSRs double the bandwidth of two-fiber BLSRs. Because they allow span switching as well as ring switching, four-fiber BLSRs increase the reliability and flexibility of traffic protection. Two fibers are allocated for working traffic and two fibers for protection, as shown in Figure 12-4. To implement a four-fiber BLSR, you must install four OC-48, OC-48 AS, or OC-192 cards at each BLSR node.



Figure 12-4 Four-Node, Four-Fiber BLSR

Four-fiber BLSRs provide span and ring switching:

• Span switching (Figure 12-5 on page 12-8) occurs when a working span fails. Traffic switches to the protect fibers between the nodes (Node 0 and Node 1 in the example in Figure 12-5) and then returns to the working fibers. Multiple span switches can occur at the same time.



• Ring switching (Figure 12-6) occurs when a span switch cannot recover traffic, such as when both the working and protect fibers fail on the same span. In a ring switch, traffic is routed to the protect fibers throughout the full ring.



#### 12.2.3 BLSR Bandwidth

BLSR nodes can terminate traffic coming from either side of the ring. Therefore, BLSRs are suited for distributed node-to-node traffic applications such as interoffice networks and access networks.

BLSRs allow bandwidth to be reused around the ring and can carry more traffic than a network with traffic flowing through one central hub. BLSRs can also carry more traffic than a path protection configuration operating at the same OC-N rate. Table 12-2 shows the bidirectional bandwidth capacities of two-fiber BLSRs. The capacity is the OC-N rate divided by two, multiplied by the number of nodes in the ring minus the number of pass-through STS-1 circuits.

OC Rate	Working Bandwidth	Protection Bandwidth	Ring Capacity
OC-12	STS1-6	STS 7-12	$6 \ge N^1 - PT^2$
OC-48	STS 1-24	STS 25-48	24 x N – PT
OC-192	STS 1-96	STS 97-192	96 x N – PT

Table 12-2 Two-Fiber BLSR Capacity

 $1. \ \ N \ \ \text{equals the number of ONS 15454 nodes configured as BLSR nodes.}$ 

2. PT equals the number of STS-1 circuits passed through ONS 15454 nodes in the ring (capacity can vary depending on the traffic pattern).

Table 12-3 shows the bidirectional bandwidth capacities of four-fiber BLSRs.

OC Rate	Working Bandwidth	Protection Bandwidth	Ring Capacity
OC-48	STS 1-48 (Fiber 1)	STS 1-48 (Fiber 2)	$48 \times N^1 - PT^2$
OC-192	STS 1-192 (Fiber 1)	STS 1-192 (Fiber 2)	192 x N – PT

Table 12-3 F	Four-Fiber BLSR	Capacity
--------------	-----------------	----------

1. N equals the number of ONS 15454 nodes configured as BLSR nodes.

2. PT equals the number of STS-1 circuits passed through ONS 15454 nodes in the ring (capacity can vary depending on the traffic pattern).

Figure 12-7 shows an example of BLSR bandwidth reuse. The same STS carries three different traffic sets simultaneously on different spans around the ring: one set from Node 3 to Node 1, another set from Node 1 to Node 2, and another set from Node 2 to Node 3.





### 12.2.4 BLSR Application Example

Figure 12-8 shows a two-fiber BLSR implementation example with five nodes. A regional long-distance network connects to other carriers at Node 0. Traffic is delivered to the service provider's major hubs.

- Carrier 1 delivers six DS-3s over two OC-3 spans to Node 0. Carrier 2 provides twelve DS-3s directly. Node 0 receives the signals and delivers them around the ring to the appropriate node.
- The ring also brings 14 DS-1s back from each remote site to Node 0. Intermediate nodes serve these shorter regional connections.

• The ONS 15454 OC-3 card supports a total of four OC-3 ports so that two additional OC-3 spans can be added at little cost.



Figure 12-8 Five-Node Two-Fiber BLSR

Figure 12-9 shows the shelf assembly layout for Node 0, which has one free slot.



Figure 12-9 Shelf Assembly Layout for Node 0 in Figure 12-8

Figure 12-10 shows the shelf assembly layout for the remaining sites in the ring. In this BLSR configuration, an additional eight DS-3s at Node IDs 1 and 3 can be activated. An additional four DS-3s can be added at Node 4, and ten DS-3s can be added at Node 2. Each site has free slots for future traffic needs.

Figure 12-10	Shelf Assembly	Layout for Nodes 1 to 4 in Figure 12-8
--------------	----------------	--



### **12.2.5 BLSR Fiber Connections**

Plan your fiber connections and use the same plan for all BLSR nodes. For example, make the east port the farthest slot to the right and the west port the farthest slot to the left. Plug fiber connected to an east port at one node into the west port on an adjacent node. Figure 12-11 shows fiber connections for a two-fiber BLSR with trunk cards in Slot 5 (west) and Slot 12 (east). Refer to the *Cisco ONS 15454 Procedure Guide* for fiber connection procedures.

Note Always plug the transmit (Tx) connector of an OC-N card at one node into the receive (Rx) connector of an OC-N card at the adjacent node. Cards display an SF LED when Tx and Rx connections are mismatched.



Figure 12-11 Connecting Fiber to a Four-Node, Two-Fiber BLSR

For four-fiber BLSRs, use the same east-west connection pattern for the working and protect fibers. Do not mix working and protect card connections. The BLSR does not function if working and protect cards are interconnected. Figure 12-12 shows fiber connections for a four-fiber BLSR. Slot 5 (west) and Slot 12 (east) carry the working traffic. Slot 6 (west) and Slot 13 (east) carry the protect traffic.



Figure 12-12 Connecting Fiber to a Four-Node, Four-Fiber BLSR

# **12.3 Path Protection**

Path Protection Configurations(PPC) provide duplicate fiber paths around the ring. Working traffic flows in one direction and protection traffic flows in the opposite direction. If a problem occurs with the working traffic path, the receiving node switches to the path coming from the opposite direction.

CTC automates ring configuration. path protection traffic is defined within the ONS 15454 on a circuit-by-circuit basis. If a path-protected circuit is not defined within a 1+1 or BLSR line protection scheme and path protection is available and specified, CTC uses path protection as the default.

A path protection circuit requires two DCC-provisioned optical spans per node. Path protection circuits can be created across these spans until their bandwidth is consumed.

Note

If a path protection circuit is created manually by TL1, data communications channels (DCCs) are not needed; therefore, path protection circuits are limited by the cross-connection bandwidth or the span bandwidth, but not by the number of DCCs.

The span bandwidth consumed by a path protection circuit is two times the circuit bandwidth, because the circuit is duplicated. The cross-connection bandwidth consumed by a path protection circuit is three times the circuit bandwidth at the source and destination nodes only. The cross-connection bandwidth consumed by an intermediate node has a factor of one.

L

The path protection circuit limit is the sum of the optical bandwidth containing 84 section data communication channels (SDCCs) or 28 line data communication channels (LDCCs), divided by two if you are using redundant TCC2/TCC2P cards. The spans can be of any bandwidth from OC-3 to OC-192. The circuits can be of any size from VT1.5 to 192c.

Figure 12-13 shows a basic four-node path protection configuration. If Node ID 0 sends a signal to Node ID 2, the working signal travels on the working traffic path through Node ID 1. The same signal is also sent on the protect traffic path through Node ID 3.

Figure 12-13 Basic Four-Node Path Protection



If a fiber break occurs (Figure 12-14), Node ID 2 switches its active receiver to the protect signal coming through Node ID 3.

Because each traffic path is transported around the entire ring, path protection configurations are best suited for networks where traffic concentrates at one or two locations and is not widely distributed. Path protection capacity is equal to its bit rate. Services can originate and terminate on the same path protection, or they can be passed to an adjacent access or interoffice ring for transport to the service-terminating location.

321



Figure 12-14 Path Protection with a Fiber Break

Figure 12-15 shows a common path protection application. OC-3 optics provide remote switch connectivity to a host Telcordia TR-303 switch. In the example, each remote switch requires eight DS-1s to return to the host switch. Figure 12-16 on page 12-18 and Figure 12-17 on page 12-18 show the shelf layout for each site.



Figure 12-15 Four-Port, OC-3 Path Protection

Node ID 0 has four DS1-14 cards to provide 56 active DS-1 ports. The other sites only require two DS1-14 cards to handle the eight DS-1s to and from the remote switch. You can use the other half of each ONS 15454 shelf assembly to provide support for a second or third ring to other existing or planned remote sites.

In the OC-3 path protection sample, Node ID 0 contains four DS1-14 cards and two OC3 IR 4 1310 cards. Six free slots can be provisioned with cards or left empty. Figure 12-16 shows the shelf setup for these card.



Figure 12-16 Layout of Node ID 0 in the OC-3 Path Protection Example in Figure 12-15

In the Figure 12-15 on page 12-17 example, Nodes IDs 1 to 3 each contain two DS1-14 cards and two OC3 IR 4 1310 cards. Eight free slots exist. They can be provisioned with other cards or left empty. Figure 12-17 shows the shelf assembly setup for this configuration example.

	4	
ſſ	DS1-14	
7	Free Slot	
Γ	Free Slot	
٢	OC3 IR 4 1310	
Γ	OC3 IR 4 1310	
Γ	TCC2/TCC2P	
Γ	Cross Connect	
7	AIC-I (Optional)	
٢	Cross Connect	
Γ	TCC2/TCC2P	
Γ	Free Slot	
6	Free Slot	
٢	Free Slot	
٢	Free Slot	
$\int$	Free Slot	
$\int$	Free Slot	

Figure 12-17 Layout of Node IDs 1 to 3 in the OC-3 Path Protection Example in Figure 12-15

L

# **12.4 Dual-Ring Interconnect**

Dual-ring interconnect (DRI) topologies provide an extra level of path protection for circuits on interconnected rings. DRI allows users to interconnect BLSRs, path protection configurations, or a path protection with a BLSR, with additional protection provided at the transition nodes. In a DRI topology, ring interconnections occur at two or four nodes.

The drop-and-continue DRI method is used for all ONS 15454 DRIs. In drop-and-continue DRI, a primary node drops the traffic to the connected ring and routes traffic to a secondary node within the same ring. The secondary node also routes the traffic to the connected ring; that is, the traffic is dropped at two different interconnection nodes to eliminate single points of failure. To route circuits on DRI, you must choose the Dual Ring Interconnect option during circuit provisioning. Dual transmit is not supported.

Two DRI topologies can be implemented on the ONS 15454:

- A traditional DRI requires two pairs of nodes to interconnect two networks. Each pair of user-defined primary and secondary nodes drops traffic over a pair of interconnection links to the other network.
- An integrated DRI requires one pair of nodes to interconnect two networks. The two interconnected nodes replace the interconnection ring.

For DRI topologies, a hold-off timer sets the amount of time before a selector switch occurs. It reduces the likelihood of multiple switches, such as:

- Both a service selector and a path selector
- Both a line switch and a path switch of a service selector

For example, if a path protection DRI service selector switch does not restore traffic, then the path selector switches after the hold-off time. The path protection DRI hold-off timer default is 100 ms. You can change this setting in the Path Protection Selectors tab of the Edit Circuits window. For BLSR DRI, if line switching does not restore traffic, then the service selector switches. The hold-off time delays the recovery provided by the service selector. The BLSR DRI default hold-off time is 100 ms, but it can be changed.

#### **12.4.1 BLSR DRI**

Unlike BLSR automatic protection switching (APS) protocol, BLSR-DRI is a path-level protection protocol at the circuit level. Drop-and-continue BLSR-DRI requires a service selector in the primary node for each circuit routing to the other ring. Service selectors monitor signal conditions from dual feed sources and select the one that has the best signal quality. Same-side routing drops the traffic at primary nodes set up on the same side of the connected rings, and opposite-side routing drops the traffic at primary nodes set up on the opposite sides of the connected rings. For BLSR-DRI, primary and secondary nodes cannot be the circuit source or destination.



A DRI circuit cannot be created if an intermediate node exists on the interconnecting link. However, an intermediate node can be added on the interconnecting link after the DRI circuit is created.

DRI protection circuits act as protection channel access (PCA) circuits. In CTC, you set up DRI protection circuits by selecting the PCA option when setting up primary and secondary nodes during DRI circuit creation.

Figure 12-18 shows ONS 15454 nodes in a traditional BLSR-DRI topology with same-side routing. In Ring 1, Nodes 3 and 4 are the interconnect nodes, and in Ring 2, Nodes 8 and 9 are the interconnect nodes. Duplicate signals are sent between Node 4 (Ring 1) and Node 9 (Ring 2), and between Node 3 (Ring 1) and Node 8 (Ring 2). The primary nodes (Nodes 4 and 9) are on the same side, and the secondary nodes (Nodes 3 and 8) provide an alternative route. In Ring 1, traffic at Node 4 is dropped (to Node 9) and continued (to Node 3). Similarly, at Node 9, traffic is dropped (to Node 4) and continued (to Node 8).





Figure 12-19 shows ONS 15454 nodes in a traditional BLSR-DRI topology with opposite-side routing. In Ring 1, Nodes 3 and 4 are the interconnect nodes, and in Ring 2, Nodes 8 and 9 are the interconnect nodes. Duplicate signals are sent from Node 4 (Ring 1) to Node 8 (Ring 2), and between Node 3 (Ring 1) and Node 9 (Ring 2). In Ring 1, traffic at Node 4 is dropped (to Node 9) and continued (to Node 3). Similarly, at Node 8, traffic is dropped (to Node 3) and continued (to Node 8).



Figure 12-19 ONS 15454 Traditional BLSR Dual-Ring Interconnect (Opposite-Side Routing)

Figure 12-20 shows ONS 15454s in an integrated BLSR-DRI topology. The same drop-and-continue traffic routing occurs at two nodes, rather than four. This is achieved by installing an additional OC-N trunk at the two interconnect nodes. Nodes 3 and 8 are the interconnect nodes.



Figure 12-20 ONS 15454 Integrated BLSR Dual-Ring Interconnect

Figure 12-21 shows an example of an integrated BLSR DRI on the Edit Circuits window.



Figure 12-21 Integrated BLSR DRI on the Edit Circuits Window

### 12.4.2 Path Protection DRI

Figure 12-22 shows ONS 15454 nodes in a traditional drop-and-continue path protection DRI topology. In Ring 1, Nodes 4 and 5 are the interconnect nodes, and in Ring 2, Nodes 6 and 7 are the interconnect nodes. Duplicate signals are sent between Node 4 (Ring 1) and Node 6 (Ring 2), and between Node 5 (Ring 1) and Node 7 (Ring 2). In Ring 1, traffic at Node 4 is dropped (to Node 6) and continued (to Node 5). Similarly, at Node 5, traffic is dropped (to Node 7) and continued (to Node 4).



Figure 12-22 ONS 15454 Traditional Path Protection Dual-Ring Interconnect

Figure 12-23 shows ONS 15454 nodes in an integrated DRI topology. The same drop-and-continue traffic routing occurs at two nodes, rather than four. This is achieved by installing an additional OC-N trunk at the two interconnect nodes.



#### Figure 12-23 ONS 15454 Integrated Path Protection Dual-Ring Interconnect

### 12.4.3 Path Protection/BLSR DRI Handoff Configurations

Path protection configurations and BLSRs can also be interconnected. In BLSR/path protectionDRI handoff configurations, primary and secondary nodes can be the circuit source or destination, which is useful when non-DCC optical interconnecting links are present. Figure 12-24 shows an example of a path protection to BLSR traditional DRI handoff.









Figure 12-25 ONS 15454 Path Protection to BLSR Integrated DRI Handoff

Figure 12-26 shows a path protection to BLSR integrated DRI handoff on the Edit Circuits window.



Figure 12-26 Path Protection to BLSR Integrated DRI Handoff on the Detailed Circuit Map

# **12.5 Comparison of the Protection Schemes**

Table 12-4 shows a comparison of the different protection schemes using OC-48 as an example.

Table 12-4Comparison of the Protection Schemes

Topology	Ring Capacity	Protected Bandwidth Between Any Two Nodes	Protection Channel Access	Dual Failure	Number of Cards
Path Protection	48 - PT	STS 1-48	Not supported	Not supported	2 x N
Two-Fiber BLSR	24 x N <sup>1</sup> - PT <sup>2</sup>	STS 1-24	STS 25-48	Not supported	2 x N
Four-Fiber BLSR	48 x N - PT	STS 1-48 (Fiber 1)	STS 1-48 (Fiber 2)	Supported	4 x N
Two-Fiber BLSR DRI	24 x N - PT	STS 1-24	STS 25-48	Supported	(2  x N) + 4
Path Protection DRI	48 - PT	STS 1-48	Not supported	Supported	(2 x N) + 4

1. N equals the number of ONS 15454 nodes configured as BLSR nodes.

2. PT equals the number of STS-1 circuits passed through ONS 15454 nodes in the ring (capacity can vary depending on the traffic pattern).

# **12.6 Subtending Rings**

Figure 12-27

The ONS 15454 supports up to 84 SONET SDCCs or 28 SONET LDCCs with TCC2/TCC2P cards. See Table 12-1 on page 12-2 for ring, SDCC, and LDCC information.

Subtending rings reduce the number of nodes and cards required, and reduce external shelf-to-shelf cabling. Figure 12-27 shows an ONS 15454 with multiple subtending rings.

ONS 15454 with Multiple Subtending Rings

Path Protected Nodes BLSR **BLSR** 

Figure 12-28 shows a path protection configuration subtending from a BLSR. In this example, Node 3 is the only node serving both the BLSR and the path protection configuration. OC-N cards in Slots 5 and 12 serve the BLSR, and OC-N cards in Slots 6 and 13 serve the path protection configuration.





Figure 12-28Path Protection Subtending from a BLSR

The ONS 15454 can support two BLSRs on the same node. This allows you to deploy an ONS 15454 in applications requiring SONET Digital Cross-connect Systems (DCSs) or multiple SONET add/drop multiplexers (ADMs).

Figure 12-29 shows two BLSRs shared by one ONS 15454. Ring 1 runs on Nodes 1, 2, 3, and 4. Ring 2 runs on Nodes 4, 5, 6, and 7. Two BLSR rings, Ring 1 and Ring 2, are provisioned on Node 4. Ring 1 uses cards in Slots 5 and 12, and Ring 2 uses cards in Slots 6 and 13.



Nodes in different BLSRs can have the same, or different node IDs.



#### Figure 12-29 BLSR Subtending from a BLSR

After subtending two BLSRs, you can route circuits from nodes in one ring to nodes in the second ring. For example, in Figure 12-29 you can route a circuit from Node 1 to Node 7. The circuit would normally travel from Node 1 to Node 4 to Node 7. If fiber breaks occur, for example between Nodes 1 and 4 and Nodes 4 and 7, traffic is rerouted around each ring: in this example, Nodes 2 and 3 in Ring 1 and Nodes 5 and 6 in Ring 2.

# **12.7 Linear ADM Configurations**

You can configure ONS 15454s as a line of add/drop multiplexers (ADMs) by configuring one set of OC-N cards as the working path and a second set as the protect path. Unlike rings, point-to-point ADMs (two-node configurations) and linear ADMs (three-node configurations) require that the OC-N cards at each node be in 1+1 protection to ensure that a break to the working line is automatically routed to the protect line.

Figure 12-30 shows three ONS 15454 nodes in a linear ADM configuration. Working traffic flows from Slot 5/Node 1 to Slot 5/Node 2, and from Slot 12/Node 2 to Slot 12/Node 3. You create the protect path by placing Slot 6 in 1+1 protection with Slot 5 at Nodes 1 and 2, and Slot 12 in 1+1 protection with Slot 13 at Nodes 2 and 3.

Figure 12-30 Linear (Point-to-Point) ADM Configuration



## **12.8 Path-Protected Mesh Networks**

In addition to single BLSRs, path protection configurations, and ADMs, you can extend ONS 15454 traffic protection by creating path-protected mesh networks (PPMNs). PPMNs include multiple ONS 15454 SONET topologies and extend the protection provided by a single path protection to the meshed architecture of several interconnecting rings. In a PPMN, circuits travel diverse paths through a network of single or multiple meshed rings. When you create circuits, you can have CTC automatically route circuits across the PPMN, or you can manually route them. You can also choose levels of circuit protection. For example, if you choose full protection, CTC creates an alternate route for the circuit in addition to the main route. The second route follows a unique path through the network between the source and destination and sets up a second set of cross-connections.

For example, in Figure 12-31 a circuit is created from Node 3 to Node 9. CTC determines that the shortest route between the two nodes passes through Node 8 and Node 7, shown by the dotted line, and automatically creates cross-connections at Nodes 3, 8, 7, and 9 to provide the primary circuit path.

If full protection is selected, CTC creates a second unique route between Nodes 3 and 9 which, in this example, passes through Nodes 2, 1, and 11. Cross-connections are automatically created at Nodes 3, 2, 1, 11, and 9, shown by the dashed line. If a failure occurs on the primary path, traffic switches to the second circuit path. In this example, Node 9 switches from the traffic coming in from Node 7 to the traffic coming in from Node 11 and service resumes. The switch occurs within 50 ms.

L



#### Figure 12-31 Path-Protected Mesh Network

PPMN also allows spans with different SONET speeds to be mixed together in "virtual rings." Figure 12-32 shows Nodes 1, 2, 3, and 4 in a standard OC-48 ring. Nodes 5, 6, 7, and 8 link to the backbone ring through OC-12 fiber. The "virtual ring" formed by Nodes 5, 6, 7, and 8 uses both OC-48 and OC-12 cards.

L



# **12.9 Four-Shelf Node Configurations**

You can link multiple ONS 15454s using their OC-N cards (that is, create a fiber-optic bus) to accommodate more access traffic than a single ONS 15454 can support. Refer to the *Cisco ONS 15454 Procedure Guide*. For example, to drop more than 112 DS-1s or 96 DS-3s (the maximum that can be aggregated in a single node), you can link the nodes but not merge multiple nodes into a single ONS 15454. You can link nodes with OC-12 or OC-48 fiber spans as you would link any other two network nodes. The nodes can be grouped in one facility to aggregate more local traffic.

Figure 12-33 on page 12-34 shows a four-shelf node setup. Each shelf assembly is recognized as a separate node in the ONS 15454 software interface and traffic is mapped using CTC cross-connect options. In Figure 12-33, each node uses redundant fiber-optic cards. Node 1 uses redundant OC-N transport and OC-N bus (connecting) cards for a total of four cards, with eight free slots remaining. Nodes 2 and 3 each use two redundant OC-N bus cards for a total of four cards, with eight free slots remaining. The four-shelf node example presented here is one of many ways to set up a multiple-node configuration.

L



Figure 12-33 Four-Shelf Node Configuration

# 12.10 STS around the Ring

You can provision STS circuits with a source endpoint and a destination endpoint on the same node, and route the traffic around a ring. The circuit source and destination can be on the same card, but you must use two different ports on the card, see Figure 12-34 on page 12-35.

Manual routing is required for STS around the ring circuits amd "Route Automatically" must be unchecked in the CTC circuit provisioning pane. STS around ring circuits created using Transaction Language 1 (TL1) are discovered by CTC and the status "COMPLETE" is displayed. STS around the ring supports circuit sizes; STS-1, 3c, 6c, 9c, 12c, 24c, 36c, 48c, and 192cs. Both unidirectional and bidirectional circuits are supported, and STS around the ring circuits are CCAT only, VCAT is not supported. STS around ring circuits are linear circuits.



# 12.11 OC-N Speed Upgrades

A span is the optical fiber connection between two ONS 15454 nodes. In a span (optical speed) upgrade, the transmission rate of a span is upgraded from a lower to a higher OC-N signal but all other span configuration attributes remain unchanged. With multiple nodes, a span upgrade is a coordinated series of upgrades on all nodes in the ring or protection group. You can perform in-service span upgrades for the following ONS 15454 cards:

- Single-port OC-12 to OC-48
- Single-port OC-12 to OC-192
- Single-port OC-12 to four-port OC-12
- Single-port OC-12 to MRC-12
- Four-port OC-12 to MRC-2.5G-4
- OC-48 to OC-192
- MRC-12 to OC-192 or OC192-XFP
- MRC-2.5G-4 to OC-192 or OC192-XFP
- OC-48 to OC192SR1/STM64IO Short Reach or OC192/STM64 Any Reach

You can also perform in-service card upgrades for the following ONS 15454 cards:

- Four-port OC-3 to eight-port OC-3
- Four-port OC-3 to MRC-2.5G-4
- Single-port OC-12 to four-port OC-12
- Single-port OC-12 to OC-48
- Single-port OC-12 to OC-192

- Single-port OC-12 to MRC-12
- Single-port OC-12 to MRC-2.5G-4
- OC-48 to MRC-12
- OC-192 to OC192-XFP
- OC-48 to OC192SR1/STM64IO Short Reach or OC192/STM64 Any Reach

Table 12-5 lists permitted upgrades for Slots 5, 6, 12, and 13 (high-speed slots).

Four-port **Eight-port** One-port Four-port **OC-48 MRC-12** Cards **0C-3 0C-3 OC-12 0C-12** OC-192 MRC-2.5G-4 Not Four-port Not Not Not Not Not Supported OC-3 supported supported supported supported supported supported Eight-port Not Not Not Not Not Not Not **OC-3**<sup>1</sup> supported supported supported supported supported supported supported One-port Not Not Not Supported Supported Not Supported OC-12 supported supported supported supported Four-port Not Not Not Not Not Not Supported OC-12<sup>2</sup> supported supported supported supported supported supported OC-48 Not Not Not Supported Supported Supported Supported supported supported supported OC-192 Not Not Supported Not Supported Supported Supported supported supported supported MRC-12 Not Not Supported Not Supported Not Supported supported supported supported supported MRC-2.5G-4 Supported Not Not Supported Supported Supported Not supported supported supported

Table 12-5Slot 5, 6, 12, and 13 Upgrade Options

1. The eight-port OC-3 is not supported in Slots 5, 6, 12, and 13.

2. The four-port OC-12 is not supported in Slots 5, 6, 12, and 13.

Table 12-6 lists permitted upgrades for Slots 1 through 4 and 14 through 17 (low-speed slots).

 Table 12-6
 Upgrade Options for Slots 1 through 4 and 14 through 17

Cards	Four-port OC-3	Eight-port OC-3	One-port OC-12	Four-port OC-12	OC-48	OC-192	MRC-2.5G-4	MRC-12
Four-port OC-3		Supported	Not supported	Not supported	Not supported		Supported	Not supported
Eight-port OC-3	Supported	—	Not supported	Not supported	Not supported		Not supported	Not supported
One-port OC-12	Not supported	Not supported		Supported	Supported		Not Supported	Supported
Four-port OC-12	Not supported	Not supported	Supported	_	Not supported		Supported	Not supported

Cards	Four-port OC-3	Eight-port OC-3	One-port OC-12	Four-port OC-12	OC-48	OC-192	MRC-2.5G-4	MRC-12
OC-48	Not supported	Not supported	Supported	Not supported	_	_	Supported	Supported
OC-192 <sup>1</sup>	_		_	—	_		—	Not supported
MRC-2.5G-4	Supported	Not supported	Not Supported	Supported	Supported		—	Not supported
MRC-12	Not supported	Not supported	Supported	Not supported	Supported		Not supported	

#### Table 12-6 Upgrade Options for Slots 1 through 4 and 14 through 17 (continued)

1. The OC-192 is not supported on Slots 1 through 4 and 14 through 17.



Replacing cards that are the same speed are not considered span upgrades. For example replacing a four-port OC-3 with an eight-port OC-3 card or replacing a single-port OC-12 with a four-port OC-12 card.

To perform a span upgrade, the higher-rate OC-N card must replace the lower-rate card in the same slot. If the upgrade is conducted on spans residing in a BLSR, all spans in the ring must be upgraded. The protection configuration of the original lower-rate OC-N card (two-fiber BLSR, four-fiber BLSR, path protection, and 1+1) is retained for the higher-rate OC-N card.

To perform a span upgrade on either the OC192-XFP or MRC-12 card with an SFP/XFP (known as pluggable port modules, PPMs, in CTC), the higher-rate PPM must replace the lower-rate PPM in the same slot. If you are using a multi-rate PPM, you do not need to physically replace the PPM but can provision the PPM for a different line rate. All spans in the network must be upgraded. The 1+1 protection configuration of the original lower-rate PPM is retained for the higher-rate PPM.

When performing span upgrades on a large number of nodes, we recommend that you upgrade all spans in a ring consecutively and in the same maintenance window. Until all spans are upgraded, mismatched card types or PPM types are present.

We recommend using the Span Upgrade Wizard to perform span upgrades. Although you can also use the manual span upgrade procedures, the manual procedures are mainly provided as error recovery for the wizard. The Span Upgrade Wizard and the Manual Span Upgrade procedures require at least two technicians (one at each end of the span) who can communicate with each other during the upgrade. Upgrading a span is non-service affecting and causes no more than three switches, each of which is less than 50 ms in duration.

Note

Span upgrades do not upgrade SONET topologies (for example, a 1+1 group to a two-fiber BLSR). Refer to the *Cisco ONS 15454 Procedure Guide* for topology upgrade procedures.

### 12.11.1 Span Upgrade Wizard

The Span Upgrade Wizard automates all steps in the manual span upgrade procedure (BLSR, path protection, and 1+1). The wizard can upgrade both lines on one side of a four-fiber BLSR or both lines of a 1+1 group; the wizard upgrades path protection configurations and two-fiber BLSRs one line at a time. The Span Upgrade Wizard requires that all working spans have DCC enabled.

The Span Upgrade Wizard provides no way to back out of an upgrade. In the case of an error, you must exit the wizard and initiate the manual procedure to either continue with the upgrade or back out of it. To continue with the manual procedure, examine the standing conditions and alarms to identify the stage in which the wizard failure occurred.

#### 12.11.2 Manual Span Upgrades

Manual span upgrades are mainly provided as error recovery for the Span Upgrade Wizard, but they can be used to perform span upgrades. Downgrading can be performed to back out of a span upgrade. The procedure for downgrading is the same as upgrading except that you choose a lower-rate card type. You cannot downgrade if circuits exist on the STSs that will be removed (the higher STSs).

Procedures for manual span upgrades can be found in the "Upgrade Cards and Spans" chapter in the *Cisco ONS 15454 Procedure Guide*. Five manual span upgrade options are available:

- Upgrade on a two-fiber BLSR
- Upgrade on a four-fiber BLSR
- Upgrade on a path protection configuration
- Upgrade on a 1+1 protection group
- Upgrade on an unprotected span

#### 12.11.3 In-Service MRC Card Upgrades

The ONS 15454 supports in-service upgrades for the following multiport fixed optics cards:

- MRC-12 multirate card
- MRC-2.5G-4 multirate card

#### 12.11.3.1 MRC-12 Multirate Card

The MRC-12 multirate card supports an in-service card upgrade from a four-port OC-3 card. The configurations on Ports 1 to 4 of the OC-3 card are migrated to Ports 1 to 4 of the MRC-12 card with OC-3 SFPs.

The MRC-12 multirate card supports an in-service card upgrade from a four-port OC-12 card. For an MRC-12 card with OC-12 SFPs, the configurations on Ports 1, 2, 3, 4 of the OC-12 card are migrated to Ports 1, 4, 7, 10 of the MRC-12 card.

The MRC-12 card also supports an in-service card upgrade from an eight-port OC-3 card. The configurations on Ports 1 to 8 of the OC-3 card are migrated to Ports 1 to 8 of the MRC-12 card with OC-3 SFPs.

When the card is upgraded, all circuits, including overhead circuits, server trails, and timing information that is provisioned on the card, are moved to the port with the appropriate signal. Note that some circuits may become partial after the card upgrade and must be configured using CTC.

Note

An existing 1+1 or BLSR protection scheme must be deleted before you perform a card upgrade and must be recreated after the upgrade is complete. Span upgrades are not supported.

Table 12-7 describes the upgrade matrix for the MRC-12 card.

Table 12-7MRC-12 Card Upgrade Matrix

Existing Card	Cross-Connect Card Type	Existing Slot Type	Existing Card Port Number	Starting Backplane STS	MRC-12 Card Port Number	Starting Backplane STS Mapping
OC-3 (4 ports)	XCVT	Drop slot	1 to 4	0, 3, 6, 9	1, 4, 7, 10	0, 48, 96, 144
OC-3 (4 ports)	XCVT	Trunk slot	1 to 4	0, 3, 6, 9	1, 2, 3, 4	0, 60, 72, 48
OC-3 (4 ports)	XC10G/XC-VXC-10G	Any slot	1 to 4	0, 3, 6, 9	1, 2, 3, 4	0, 60, 72, 48
OC-3 (8 ports)	XCVT	Not supported	—	—	—	
OC-3 (8 ports)	XC10G/XC-VXC-10G	Drop slot Note OC-3 (8 ports) card is not supported in trunk slots for the XC10G and XC-VXC-10G cards.	1 to 8	0, 3, 6, 9, 12, 15, 18, 21	1 to 8	0, 60, 72, 48, 108, 120, 96, 132
OC-12 (4 ports)	XCVT	Not supported				_
OC-12 (4 ports)	XC10G/XC-VXC-10G	Drop slot Note OC-12 (4 ports) card is not supported in trunk slots for the XC10G and XC-VXC-10G cards.	1 to 4	0, 12, 24, 36	1, 4, 7, 10	0, 48, 96, 144

#### 12.11.3.2 MRC-2.5G-4 Multirate Card

The MRC-2.5G-4 card supports an in-service card upgrade from a four-port OC-3 card. The configurations on Ports 1 to 4 of the OC-3 card are migrated to Ports 1 to 4 of the MRC-2.5G-4 card with OC-3 SFPs.

The MRC-2.5G-4 card also supports an in-service card upgrade from a four-port OC-12 card. For an MRC-2.5G-4 card with OC-12 SFPs, the configurations on Ports 1 to 4 of the OC-12 card are migrated to Ports 1 to 4 of the MRC-2.5G-4 card.

When the card is upgraded, all circuits, including overhead circuits, server trails, and timing information that is provisioned on the card, are moved to the port with the appropriate signal. Note that some circuits may become partial after the card upgrade and must be configured using CTC.

<u>Note</u>

An existing 1+1 or BLSR protection scheme must be deleted before you perform a card upgrade and must be recreated after the upgrade is complete. Span upgrades are not supported.

Table 12-8 describes the upgrade matrix for the MRC-2.5G-4 card.

Table 12-8MRC-2.5G-4 Card Upgrade Matrix

Existing Card	Cross-Connect Card Type	Existi	ng Slot Type	Existing Card Port Number	Starting Backplane STS	MRC-2.5G-4 Card Port Number	Starting Backplane STS Mapping
OC-3 (4 ports)	XCVT	Drop	slot	1 to 4	0, 3, 6, 9	1 to 4	0, 48, 96, 144
OC-3 (4 ports)	XC10G/XC-VXC-10G	5		1 to 4	0, 3, 6, 9	1 to 4	0, 48, 96, 144
OC-3 (8 ports)	XCVT/XC10G/XC-VXC-10G	Not supported			—		—
OC-12 (4 ports)	XCVT	Not supported			—		—
OC-12 (4 ports)	XC10G/XC-VXC-10G	Drop : Note	slot OC-12 (4 ports) card is not supported in trunk slots for the XC10G and XC-VXC-10 G cards.	1 to 4	0, 12, 24, 36	1 to 4	0, 48, 96, 144

The card upgrade procedure automatically provisions PPMs, modifies the port count, adjusts bandwidth pools, and provisions VT circuits. For more information on how to perform in-service card upgrades, refer to the *Cisco ONS 15454 Procedure Guide*.

# 12.12 In-Service Topology Upgrades

Topology upgrades can be performed in-service to convert a live network to a different topology. An in-service topology upgrade is potentially service-affecting, and generally allows a traffic hit of 50 ms or less. Traffic might not be protected during the upgrade. The following in-service topology upgrades are supported:

- Unprotected point-to-point or linear ADM to path protection
- Point-to-point or linear ADM to two-fiber BLSR
- Path protection to two-fiber BLSR
- Two-fiber to four-fiber BLSR
- Node addition or removal from an existing topology

You can perform in-service topology upgrades irrespective of the service state of the involved cross-connects or circuits; however, a circuit must have a DISCOVERED status.

Circuit types supported for in-service topology upgrades are:

- STS, VT, and VT tunnels
- Virtual concatenated circuits (VCAT)
- Unidirectional and bidirectional
- Automatically routed and manually routed
- CTC-created and TL1-created
- Ethernet (unstitched)
- Multiple source and destination (both sources should be on one node and both drops on one node)

You cannot upgrade stitched Ethernet circuits during topology conversions. For in-service topology upgrade procedures, refer to the "Convert Network Configurations" chapter in the *Cisco ONS 15454 Procedure Guide*. For procedures to add or remove a node, refer to the "Add and Remove Nodes" chapter of the *Cisco ONS 15454 Procedure Guide*.

Note

A database restore on all nodes in a topology returns converted circuits to their original topology.

Note

Open-ended path protection and DRI configurations do not support in-service topology upgrades.

#### 12.12.1 Unprotected Point-to-Point or Linear ADM to Path Protection

CTC provides a topology conversion wizard for converting an unprotected point-to-point or linear ADM topology to path protection. This conversion occurs at the circuit level. CTC calculates the additional path protection circuit route automatically or you can do it manually. When routing the path protection circuit, you can provision the USPR as go-and-return or unidirectional.

When performing an in-service topology upgrade on a configuration with VCAT circuits, CTC allows you to select member circuits to upgrade individually. When upgrading VT tunnels, CTC does not convert the VT tunnel to path protection, but instead creates a secondary tunnel for the alternate path. The result is two unprotected VT tunnels using alternate paths.

To convert from point-to-point or linear ADM to a path protection, the topology requires an additional circuit route to complete the ring. When the route is established, CTC creates circuit connections on any intermediate nodes and modifies existing circuit connections on the original circuit path. The number and position of network spans in the topology remains unchanged during and after the conversion.

Figure 12-35 shows an unprotected point-to-point ADM configuration converted to a path protection. An additional circuit routes through Node 3 to complete the path protection.



Figure 12-35 Unprotected Point-to-Point ADM to Path Protection Conversion

#### 12.12.2 Point-to-Point or Linear ADM to Two-Fiber BLSR

A 1+1 point-to-point or linear ADM to a two-fiber BLSR conversion is manual. You must remove the protect fibers from all nodes in the linear ADM and route them from the end node to the protect port on the other end node. In addition, you must delete the circuit paths that are located in the bandwidth that will become the protection portion of the two-fiber BLSR (for example, circuits in STS 25 or higher on an OC-48 BLSR) and recreate them in the appropriate bandwidth. Finally, you must provision the nodes as BLSR nodes.

To complete a conversion from an unprotected point-to-point or linear ADM to a two-fiber BLSR, use the CTC Convert Unprotected/Path Protection to BLSR wizard from the Tools > Topology Upgrade menu.

### 12.12.3 Path Protection to Two-Fiber BLSR

CTC provides a topology conversion wizard to convert a path protection to a two-fiber BLSR. An upgrade from a path protection to a two-fiber BLSR changes path protection to line protection. A path protection can have a maximum of 16 nodes before conversion. Circuits paths must occupy the same time slots around the ring. Only the primary path through the path protection is needed; the topology conversion wizard removes the alternate path protection path during the conversion. Because circuit paths can begin and end outside of the topology, the conversion might create line-protected segments within path protection paths of circuits outside the scope of the ring. The physical arrangement of the ring nodes and spans remains the same after the conversion.

## 12.12.4 Two-Fiber BLSR to Four-Fiber BLSR

CTC provides a wizard to convert two-fiber OC-48 or OC-192 BLSRs to four-fiber BLSRs. To convert the BLSR, you must install two OC-48 or OC-192 cards at each two-fiber BLSR node, then log into CTC and convert each node from two-fiber to four-fiber. The fibers that were divided into working and protect bandwidths for the two-fiber BLSR are now fully allocated for working BLSR traffic.

# 12.12.5 Add or Remove a Node from a Topology

You can add or remove a node from a linear ADM, BLSR, or path protection configuration. Adding or removing nodes from BLSRs is potentially service affecting; however, adding and removing nodes from an existing 1+1 linear ADM or path protection configuration does not disrupt traffic. CTC provides a wizard for adding a node to a point-to-point or 1+1 linear ADM. This wizard is used when adding a node between two other nodes.

# **12.13 Overlay Ring Circuits**

An overlay ring configuration consists of a core ring and subtended rings (Figure 12-36). An Overlay Ring Circuit routes traffic around multiple rings in an overlay ring configuration, passing through one or more nodes more than once. This results in multiple cross-connections on the nodes connecting the core ring to the subtended rings. For example, a customer having a core ring with cross-connects provisioned using TL1 can create create cross-connects on subtended rings, due to a business need, without having to hamper the existing cross-connects on the core ring. This circuit can be either protected or unprotected.

A typical path protected overlay ring configuration is shown in Figure 12-36, where the circuit traverses the nodes B, D, and F twice resulting in two cross-connections on these nodes for the same circuit. In Figure 12-36, the circuits on the OC-12 path are unprotected. The DS3 drop traffic is protected on the drop nodes by provisioning a primary and secondary destination, making it a path protected circuit.



Overlay ring supports circuit sizes; STS-1, 3c, 6c, 9c, 12c, 24c, 36c, 48c, and 192cs. Both unidirectional and bidirectional circuits are supported. Overlay ring circuits are contiguous concatenated (CCAT) and not virtual concatenated (VCAT) circuits.

Manual routing is mandatory while provisioning the overlay ring circuit. Overlay ring circuits created using Transaction Language 1 (TL1) are discovered by CTC and the status "DISCOVERED" is displayed. If the overlay ring circuit is deleted, the cross-connects on the core ring and subtended rings get deleted. Cross-connects on a subtended ring can be deleted through TL1 but would reflect as a partial overlay ring circuit in CTC, i.e. core ring will continue having cross-connects.